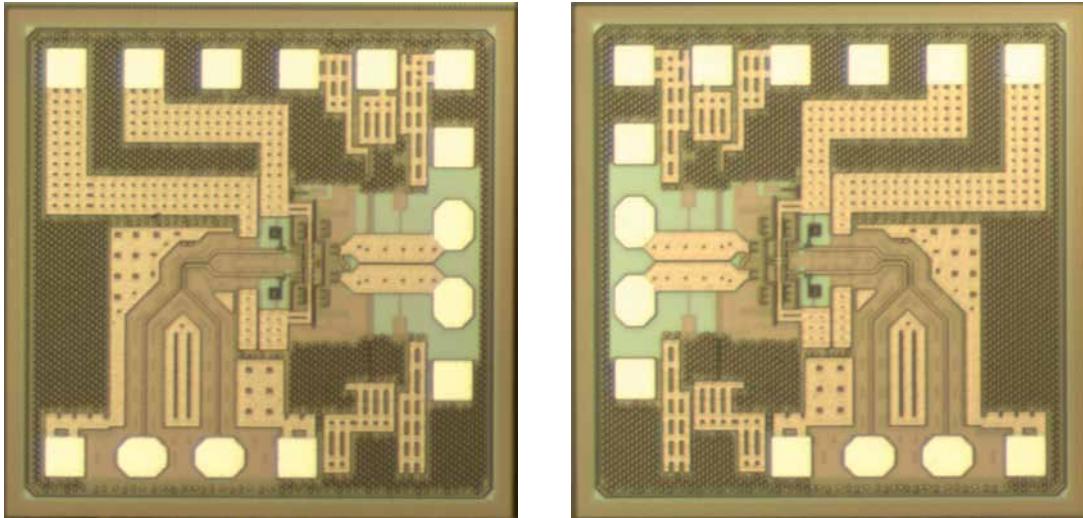


LOW-POWER 32 GBd LINEAR OPTICAL MODULATOR DRIVER



AT A GLANCE

32 GBd linear differential driver
for telecom and datacom
applications

Features

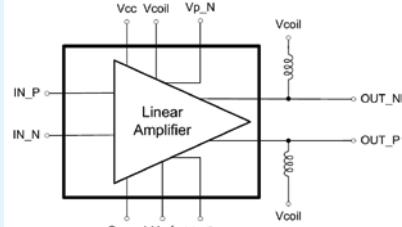
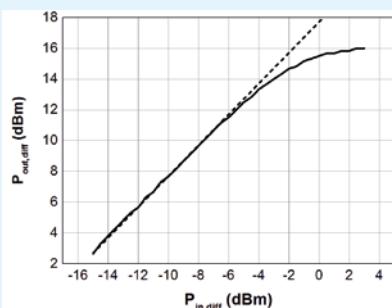
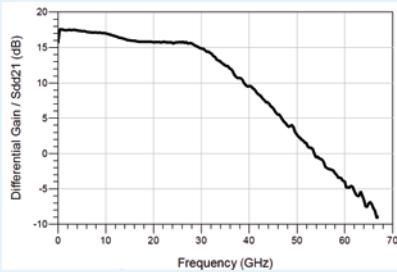
- Differential input and differential output
- Open-collector outputs
- Ultra-low power, 270 mW
- Linear Driver
- 3.0V_{pp} differential output at $2 \times 25\Omega$ loads
- Integrated output peak-level detectors
- 90°-bended RF input, mirrored IC available

Low-power open-collector SiGe Driver IC

HHI provides open-collector SiGe linear driver IC for InP Mach-Zehnder modulator. Its differential output is suitable to drive InP Mach-Zehnder modulator having $2 \times 25\Omega$. It integrates output peak-level detectors and consumes 270 mW per channel. It enables the electro-optical module to consume the lowest power.

Applications

- Mach-Zehnder modulator driver
- Supports NRZ, PAM-4 Signals
- Broadband signal amplification



Circuit Block Diagram

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Specifications

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Bandwidth	BW		30		GHz	
Power	P		270		mW	
Data Rate	DR		32		Gb/s	
Rise / fall time	t_r/t_f		10		ps	20 % - 80 %
Gain*			17.5		dB	Differential S_{21} , $Z_{in,diff} = 100\Omega$, $Z_{Load,diff} = 50\Omega$
Group Delay Distortion*	GD		± 5		ps	
Jitter (rms)			523		fs	
Jitter(p-p)			3.47		ps	
Differential Input Signal	$V_{IN,P} - V_{IN,N}$		600		mV _{pp}	AC-coupled
Differential Output Signal	$V_{OUT,P} - V_{OUT,N}$		3000		mV _{pp}	2 x 25 Ω load
P_{1dB}	P_{1dB}	13,6		14,4	dBm	output-referred, $Z_{Load,diff} = 50\Omega$
THD	THD		3.7		%	1 GHz, 3 V _{pp} output conditions
CMRR*	CMRR		14		dB	up to 20 GHz
Input Reflection*	S_{dd11}	DC < f < 8 GHz 8 GHz < f < 24 GHz 24 GHz < f < BW	-19 -9 -8		dB	Differential input
Output Peak-level detector			170 mV		V/V _{pp,diff}	$Z_{Load,diff} = 50\Omega$, each output (V _{p_N} , V _{p_P}) referenced to Vref
Operation Temperature			40		°C	

* denotes that measurements were carried out at room temperature condition, 23°C. Unless noted, measurement temperature is 40°C.

